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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/989,808	11/19/2001	Manfred Bartz	CYPR-CD01169M	1806

7590 10/19/2005
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EXAMINER

WHITMORE, STACY

ART UNIT	PAPER NUMBER
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2825

DATE MAILED: 10/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/989,808	Applicant(s) BARTZ ET AL.	
	Examiner Stacy A. Whitmore	Art Unit 2825	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 19 July 2005.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-35 is/are pending in the application.
4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-8, 11=18, 21-27, 30, and 34 is/are rejected.
- 7) Claim(s) 9, 10, 19, 20, 28, 29, 31-33 and 35 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 19 November 2001 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

1. Claims 1-4, 13-15, and 22-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Williams (US Patent 6,631,508).
2. As for claims 1-2, 4, 13-15, and 22-24, Williams discloses the invention as claimed, including a method/system comprising computer readable medium having instructions/a processor coupled to a bus for facilitating circuit design, comprising: Receiving inputs to configure said circuit, said circuit implementing a plurality of modules [abstract, col. 2, lines 61-67, col. 3, lines 1-3]; Generating at least two/three elements selected from the group consisting of: an API(s) for programming an operation of a first of said modules [abstract, col. 2, lines 61-67, col. 3, lines 1-3]; source code for realizing said plurality of modules in hardware [abstract, col. 2, lines 61-67, col. 3, lines 1-3]; an interrupt vector table having a call to in interrupt

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service routine for said first of said modules []; and a data sheet for said circuit [col. 4, lines 15-17], wherein said circuit comprises said plurality of modules [abstract, col. 2, lines 61-67, col. 3, lines 1-3];

[clam 3] An API for programming said operation of said first of said modules [abstract, col. 2, lines 61-67, col. 3, lines 1-3], said source code for realizing said plurality of modules in said hardware [abstract, col. 2, lines 61-67, col. 3, lines 1-3]; said interrupt vector table having said call to said interrupt service routine for said first of said modules, and said data sheet for said circuit, wherein said circuit comprises said plurality of modules [col. 4, lines 15-17];

Wherein said inputs to configure said circuit comprise parameters for said modules [col. 4, lines 48-49];

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 5-8, 11-12, 16-18, 21, 25-27, 30, and 34, are rejected under 35 U.S.C. 103(a) as being unpatentable over Williams (US Patent 6,631,508) in view of Perry (US Patent 6,678,877).

4. As for claims 5-8, 11-12, 16-18, 21, 25-27, 30, 34, Williams discloses the invention substantially as claimed, including the method and system for facilitating circuit design as cited above in the rejection of claims 1-4, 13-15, 22-24.

Williams does not specifically disclose

Wherein said inputs to configure said circuit comprise placements for said modules in a GUI describing resources operable to implement said modules;

Wherein said datasheet is an HTML document;

Wherein said datasheet is generated dynamically from a plurality of XML files;

Wherein said plurality of XML files comprise module files that describe said modules and a device description file that describes resources operable to implement said modules;

Wherein said source code is derived from the placement of said modules in a GUI that describes resources operable to implement said modules;

Wherein said source code is derived from parameters of said modules, said parameters being inputs to configure said circuit;

Wherein said system comprises a GUI for displaying representations of resources operable to implement said modules;

Perry discloses

Wherein said inputs to configure said circuit comprise placements for said modules in a GUI describing resources operable to implement said modules [fig. 27, element 2730, col. 15, lines 13-16];

Wherein said datasheet is an HTML document [fig. 9, datasheet, col. 15, lines 21-28, col. 17, lines 49-51];

Wherein said datasheet is generated dynamically from a plurality of XML files [col. 9, lines 27-36];

Wherein said plurality of XML files comprise module files that describe said modules and a device description file that describes resources operable to implement said modules [col. 9, lines 27-36, col. 8, lines 54-64];

Wherein said resources are implemented in a microcontroller [];

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Wherein said source code is operable to program a microcontroller to implement said modules [];

Wherein said source code is derived from the placement of said modules in a GUI that describes resources operable to implement said modules [fig. 27, col. 8, line 64 – col. 10, line 15];

Wherein said source code is derived from parameters of said modules, said parameters being inputs to configure said circuit [fig. 27, col. 8, line 64 – col. 10, line 15];

Wherein said API comprises a function call, said call operable to cause said first of said modules to perform a predetermined function [];

Wherein said API comprises an interrupt service routine for a first of said modules [];

Wherein said system comprises a GUI for displaying representations of resources operable to implement said modules [fig. 27, element 2730, col. 15, lines 13-16];

Automatically inserting, in said interrupt vector table, a call to said interrupt service routine, said interrupt service routine servicing a module placed in said GUI, wherein said call is inserted in said interrupt vector table based on inputs to configure said circuit [].

It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of Williams and Perry because utilizing Perry's GUI, datasheet (HTML document), generating the datasheet form XML, for facilitating design would have improved Williams system by incorporating file formats that are computer network oriented, thereby improving availability of computer resources for design [see Perry, col. 18].

5. Applicant's arguments, filed 7/19/2005, with respect to the rejection(s) of claim(s) 1-35 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Williams and Perry.

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6. Claims 9-10, 19-20, 28-29, 31-33, and 35 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

7. The following is a statement of reasons for the indication of allowable subject matter: The prior art of record fails to disclose either singularly or in combination the invention as claimed, including Wherein said resources are implemented in a microcontroller; Conditionally compiling a first of said APIs based on said inputs to configure said circuit ; Wherein said source code is operable to program a microcontroller to implement said modules; Conditionally compiling a first of said APIs based on said inputs to configure said circuit; Wherein said API comprises a function call, said call operable to cause said first of said modules to perform a predetermined function; Automatically inserting, in said interrupt vector table, a call to said interrupt service routine, said interrupt service routine servicing a module placed in said GUI, wherein said call is inserted in said interrupt vector table based on inputs to configure said circuit, wherein said API comprises an interrupt service routine for a first of said modules;

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stacy A. Whitmore whose telephone number is (571) 272-1685. The examiner can normally be reached on Monday-Thursday, alternate Friday 6:30am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (571) 272-1907. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Stacy A Whitmore

Primary Examiner

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A handwritten signature in black ink, appearing to read 'Stacy A. Whitmore', written in a cursive style.

SAW

October 17, 2005